

An Enhanced Phase locked loop with demodulation of FM signals

*Pathan Mohd Basha khan and **Pathan Mohd Aziz khan
 * JJT University, Rajasthan
 **JJT University, Rajasthan

Abstract In this paper present a phase locked loop (PLL) system used in commercial applications because of their low cost. Phase locked loop used to track the phase and the frequency of the carrier component of an incoming signal. This paper discusses about the implementation of PLL device and also presents the VCO, phase comparator and Loop Filter based through demodulation of FM signal. PLL is beneficial device for the demodulation of angle modulated signals, especially when signal to noise ratio is poor and improve the threshold properties also.

Index Terms - Phase locked loop, Voltage controlled oscillator, Frequency Modulating frequency, Low Pass filter, Phase error.

I. INTRODUCTION

PLL is a negative feedback system which performs tracking of phase and frequency components from the input carrier signal. PLL is used now –a-days in commercial FM receivers, because of their low cost and superior performance. A PLL Circuit diagram is shown in Fig.1. It consist of three components

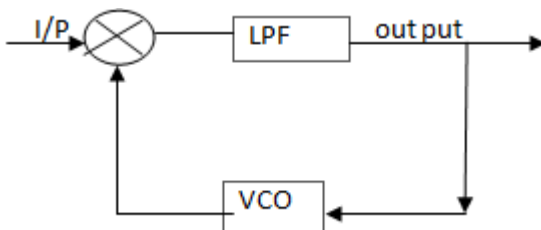


Fig.1 phase locked loop

- A voltage controlled oscillator
- A multiplier serving as a phase detector (or) a phase comparator
- A loop filter which is a low pass filter.

The frequency of VCO can be controlled by the external voltage. In a VCO the oscillation frequency varies linearly with its input voltage. The output of the multiplier is passed through a low pass filter and then applied to the input of the VCO. This voltage then charges the frequency of the VCO and keeps the loop in locked condition. Initially the control voltage to VCO is zero then is adjusted so that

- I) the frequency of the VCO is exactly made equal to the un-modulated carrier frequency F_c ;
- II) The VCO output has a phase Shift of 90o with respect to the un-modulated carrier wave.

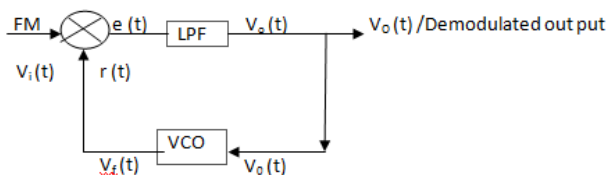


Fig.2

Suppose that the input signal applied to PLL is an FM wave

$$S(t) = A_c \sin [2\pi fct + \phi_1(t)] \text{-----} \text{eq.1}$$

With a modulating signal $m(t)$ we have

$$\phi_1(t) = 2\pi kf \int_0^t m(t) dt \text{-----} \text{eq.2}$$

If $r(t)$ denotes the o/p of VCO then

$$R(t) = A_v \cos [2\pi fct + \phi_2(t)] \text{-----} \text{eq.3}$$

If $v(t)$ is the control voltage applied as i/p voltage to VCO then

$$\phi_2(t) = 2\pi kv \int_0^t v(t) dt \text{-----} \text{eq. 4}$$

Where K_v is the frequency sensitivity of VCO and K_v is measured in HZ/volt.

The incoming FM wave $s(t)$ and the VCO o/p, $r(t)$ are the two inputs to multiplier. The o/p $e(t)$ which is i/p to the loop filter will be

$$E(t) = K_m [s(t)] [r(t)] \text{-----} \text{eq.5}$$

Where k_m is multiplier gain

$$E(t) = K_m [A_c \sin (2\pi fct + \phi_1(t))] [A_v \cos (2\pi fct + \phi_2(t))] \text{-----} \text{eq.6}$$

Now

$$E(t) = K_m [A_c A_v / 2 [\sin (4\pi fct + \phi_1(t) + \phi_2(t)) + \sin (\phi_1(t) - \phi_2(t))] \text{-----} \text{eq.7}$$

$$= \frac{1}{2} K_m A_c A_v [\sin (4\pi fct + \phi_1(t) + \phi_2(t)) + \sin (\phi_1(t) - \phi_2(t))] \text{-----} \text{eq.8}$$

This is the input to the loop filter which is a low pass filter which will attenuate the first term in the above eq. which is high frequency.

$$E(t) = \frac{1}{2} K_m A_c A_v \sin [\phi_1(t) - \phi_2(t)] \text{-----} \text{eq.9}$$

$$= \frac{1}{2} K_m A_c A_v \sin [\phi_e(t)]$$

Where $\phi_e(t)$ is the phase error

$$\text{Therefore } \phi_e(t) = \phi_1(t) - \phi_2(t) \text{-----} \text{eq.10}$$

The loop filter operates on its i/p $e(t)$ to produce the o/p

$$v(t) = \int_{-\infty}^t e(\tau) h(t - \tau) d\tau \text{-----} \text{eq.11}$$

Where $h(t)$ is the impulse response of the filter. Substitute $v(t)$ value in Eq.10

$$\phi_e(t) = \phi_1(t) - 2\pi K_v \int_0^t \int_{-\infty}^{\tau} e(\tau) h(t - \tau) d\tau dt \text{-----} \text{eq.12}$$

Differentiating eq12 with respect to time

$$d/dt \phi_e(t) = d/dt \phi_1(t) - 2\pi K_m K_v A_c A_v \int_{-\infty}^t \sin [\phi_e(\tau)] h(t - \tau) dt \text{-----} \text{eq.13}$$

Therefore

$$d/dt \phi_e(t) = d/dt \phi_1(t) - 2\pi K_o \int_{-\infty}^t \sin [\phi_e(\tau)] h(t - \tau) dt \text{-----} \text{eq.14}$$

Where K_o is a loop Parameter defined as $K_o = K_m A_c A_v$

II. NONLINEAR MODEL OF PLL

The equation 14 suggest the nonlinear model as shown in fig 2. This model includes the relationship between $v(t)$ and $E(t)$ as represented by eq.9 and eq.12 this model resembles the block diagram shown in fig.3. the multiplier

is replaced by subtractor and a sinusoidal nonlinearity, and VCO by an integrator.

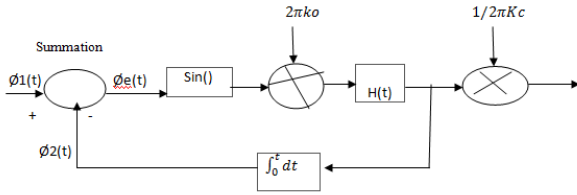


Fig.3 Non-linear model of phase locked loop

III. LINEAR MODEL OF PHASE LOCKED LOOP

The phase locked loop is said to be in phase lock when the phase error $\phi_e(t)$ is zero when phase error $\phi_e(t)$ is less than one radian, we may use the approximation

$$\sin[\phi_e(t) \approx \phi_e(t)] \longrightarrow \text{eq.15}$$

The above eq. is accurate to within 4% for $\phi_e(t)$ less than 0.5 rad. This approximation tells that we can consider loop near to phase lock when $\phi_e(t)$ is less than 0.5 rad. This approximation allows to disregard the sinusoidal nonlinearity. The fig.4 shows the linear model of phase locked loop eliminating sinusoidal non-linearity.

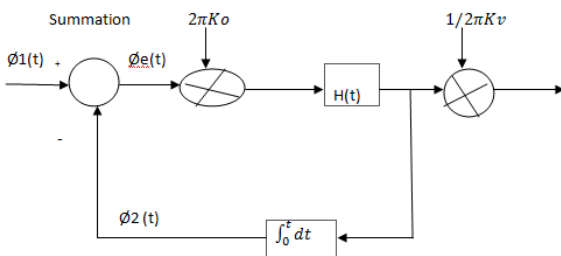


Fig.4 linear model of phase locked loop

From the linear model of phase locked loop we can write

$$\phi_2(t) = 2\pi K_v \int_0^t v(t) dt \longrightarrow \text{eq.16}$$

We know that

$$\phi_e(t) = \phi_1(t) - \phi_2(t) \longrightarrow \text{eq.17}$$

Substituting value of $\phi_2(t)$ given by the eq.16 in eq.17 we get

$$\phi_e(t) = \phi_1(t) - 2\pi K_v \int_0^t v(t) dt \longrightarrow \text{eq.18}$$

$$2\pi K_v \int_0^t v(t) dt = \phi_1(t) - \phi_e(t) \longrightarrow \text{eq.19}$$

Assuming again small error $\phi_e(t)$

$$2\pi K_v \int_0^t v(t) dt = \phi_1(t) \longrightarrow \text{eq.20}$$

For FM wave input:

$$\phi_1(t) = 2\pi K_f \int_0^t m(t) dt \longrightarrow \text{eq.21}$$

$$2\pi K_v \int_0^t v(t) dt = 2\pi K_f \int_0^t m(t) dt \longrightarrow \text{eq.22}$$

Differentiating both sides with respect to time t;

$$K_v [v(t)] = K_f [m(t)]$$

$V(t)$ is directly proportional to $m(t)$

Thus the o/p $v(t)$ of the low pass loop filter is proportional to the original modulating signal $m(t)$ or in other words the input FM signal is demodulated and at the o/p of loop filter, we get the modulating signal.

An interesting feature of PLL, FM demodulator is that the bandwidth of the incoming FM wave can be much wider than that of the loop filter. The baseband of the loop filter has a bandwidth equal to that of the modulating signal i.e. the control signal of the VCO has the bandwidth of the message signal $m(t)$; but the VCO o/p is a wideband

frequency modulated wave whose instantaneous frequency tracks that of the incoming FM wave.

IV. CONCLUSION

In conclusion firstly the loop filter, VCO and multiplier are investigated of PLL. After investigation multiplier of PLL are replaced by subtractor and sinusoidal nonlinearity and the VCO by an integrator in non linear model of PLL. If the phase error is less than one radian we may approximation allows disregarding the sinusoidal nonlinearity. The input FM signal is demodulated and at the o/p of loop filter, we get the modulating signal. The baseband of the loop filter has a bandwidth equal to that of the modulating signal i.e. the control signal of the VCO has the bandwidth of the message signal $m(t)$; but the VCO o/p is a wideband frequency modulated wave whose instantaneous frequency tracks that of the incoming FM wave.

REFERENCES

- [1] F.M. Gardner, "Phase lock Techniques", 2nd Ed. New York: Wiley, 1966.
- [2] B. P. Lathi, "Communication Systems", John Wiley and Sons, Inc., 1968.
- [3] T.G. Thomas, S. C. Sekhar, "Communication Theory", Tata-McGraw Hill 2005.
- [4] Haykin, Simon [Ed]. (2001). "Communication Systems", 4th Ed.
- [5] Der, Lawrence, Ph.D., "Frequency Modulation (FM)" Tutorial.
- [6] A. Blanchard, "Phase Locked Loops", John Wiley & Sons, 1976.
- [7] B. Biwa's, "Range Extension of a Phase Locked Loop". *Tran Com.*, vol. 21, pp. 293, April 1973.
- [8] V. Frazier, "Phase Lock Loop Frequency Acquisition Study", *Tran. I.R.E.*, vol. 8, pp. 210, September 1962.
- [9] A. V. Bala krishnan, "Advances in Communication Systems", New York: Academic Press, pp. 98-102, 1965.
- [10] M. Schwartz, W. R. Bennett, S. Stein, "Communication Systems and Techniques", New York: McGraw-Hill, pp. 161-163, 1966.
- [11] C. J. Byrne, "Properties and design of the phase-controlled oscillator with a saw tooth comparator", *Bell Syst. Tech. J.*, vol. 41, pp. 559-602, 1962.
- [12] W. C. Lindsey, R. C. Tausworthe, "A survey of phase-locked theory", *Conf. Rec. IEEE Int. Commun. Conf.*, 1967-June.
- [13] J. J. Stiffler, "On the selection of signals for phase-locked loops", *IEEE Trans. Commun. Technol.*, vol. COM-16, pp. 239-244, Apr. 1968.
- [14] F. Carassa, F. Rocca, "Advances in phase-lock demodulation", *IEEE Trans. Commun. Technol.*, vol. COM-18, pp. 182-191, June 1970.
- [15] D. P. Olsen, J. C. Lindenlaub, "A phase lock loop with an extended linear range phase detector", *Conf. Rec. Mervin J. Kelly Commun. Conf.*, 1970-Oct.
- [16] A. J. Viterbi, "Acquisition and tracking behavior of phase-locked loops", *Proc. Symp. Active Networks and Feedback Systems*, vol. 10, pp. 583-619, 1960-Apr.
- [17] F. D. Natali, W. J. Walbesser, "Interference rejection in a phase-locked loop with decision feedback", *EASCON Rec.*, pp. 187-192, 1968-Sept.
- [18] J. C. Schmoock, "A monolithic PLL system for FSK and tone detection", *Proc. 7th ASILMOR Conf. Circuits Systems and Computers*, 1973-Nov.